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PHYS 580

11/19/20

Dr. Kwon

Final Project: Development and Management Plan

1. Design Plan:
   1. Define the Problem (Scenario):
      1. Create a photoresistor theremin that can be calibrated and then used to play, record, and play back recorded files in LabVIEW.
   2. Inputs:
      1. Wait (ms) (Possibly)
      2. Start Calibration
      3. Log Calibration Data
      4. Restart/Exit
      5. # of Major Scale
      6. Volume Knob
      7. Harmony Button
      8. Octave Button
      9. Record
      10. Stop Recording
      11. Filename Out
      12. Playback
      13. Filename In
      14. Restart
      15. Exit
   3. Outputs:
      1. Wait (ms) (Possibly)
      2. # of Major Scale
      3. Output Audio Frequency
      4. Volume Knob
      5. Harmony LED
      6. Octave LED
      7. Filename Out
      8. Recording LED
      9. Playback LED
   4. Additional Requirements:
      1. AI Voltage Task (myDAQ/ai0:1)
      2. AO Voltage Task (myDAQ/ao0)
      3. Digital Input (myDAQ/dio0:1)
      4. Voltage Out (10 V)
      5. # of Notes in Scale (7)
      6. Set Voltage Out to 0 V After While Loop Stops
      7. Calculate Frequencies as a Function of Inputted Scale Number Using Equal Temperament Tuning Relative to Middle C
      8. Using iPhone Flashlight as Light Source for Photoresistor
      9. Made 6 Evenly Spaced Lines on Paper for Calibrating V\_PR
      10. Circuit Components: Red LED, Photoresistor, 220 Ohm Resistor, Potentiometer, 2 Buttons, Breadboard Jumper Wires
   5. Algorithm:
      1. Main Menu
         1. Prompt user if they want to start calibration
         2. If yes, go to “Calibrate” state, else go to “Reset and Restart or Stop” state
      2. Calibrate
         1. Prompt user for # of Major Scale
         2. Use user input to calculate frequencies of all notes in the scale
         3. Prompt user for when they want to record each of the 6 V\_PR readings to calibrate the theremin
         4. Go to “Practice” State
      3. Practice
         1. Read V\_PR and output the corresponding audio frequency
         2. If Harmony Button is pressed, add the frequency one third above the current frequency in the inputted scale
         3. If the Octave Button is pressed, double the frequency
         4. If the Record Button is pressed, start writing the data to a log file with a user-inputted path and filename until the Stop Recording Button is pressed. Trim beginning and ending of recording
         5. If the Playback Recoding button is pressed, go to “Playback Recording” state
         6. If the Exit Button is pressed, go to the “Reset and Restart or Stop” state
      4. Playback Recording
         1. Prompt the user for the path and filename of the recording to be played
         2. Play audio file
         3. Go to “Practice” state
      5. Reset and Restart or Stop
         1. Prompt user if they want to restart or stop VI. If they choose restart, go to “Main Menu” state, else stop the while loop, send an Output Voltage of 0V to the physical circuit, and clear tasks
   6. Front Panel:
      1. \*Note: The VI is currently in the initial debugging stage and the Front Panel is intentionally missing features so that testing can take place

A picture containing diagram

Description automatically generated

1. Management and Testing Plan including Milestones, Timeline and Tracking Plan including Weekly Benchmarks to Track (to be checked during the lab)
   1. Development Plan (final) to Dropbox: 8am, Nov. 24 (Tuesday)
   2. Weekly Check: Week 15 during the lab
      1. Nov. 26:
         1. Add audio features to VI and get recording and playback features to work
      2. Dec. 3:
         1. Polish VI
   3. VI Demo & Presentation (15 min): Week 16 (Dec. 10)
   4. VI Screenshot, Description, and PDF of the Full VI to Discussion (2pm Dec. 10)
   5. Final VI to Dropbox (4:45pm Dec. 10)